

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 67161-058	SERIAL NO. 10/625,644	
				APPLICANT Hideto HIDAKA		
				FILING DATE July 24, 2003	GROUP	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
<i>JM</i>	A1	US 2003/0081450	05/01/2003	HIDAKA		
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FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation
						Yes
						No
OTHER ART (including Author, Title, Date, Pertinent Pages, ETC.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
<i>JM</i>		SCHEUERLEIN, et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell" IEEE International Solid-State Circuits Conference (2000) TA 7.2.				
<i>JM</i>		DURLAM, et al. "Nonvolatile RAM Based on Magnetic Tunnel Junction Elements" IEEE International Solid-State Circuits Conference (2000) TA 7.3.				
<i>JM</i>		NAJI, et al. "A 256kb 3.0V 1T1MJT Nonvolatile Magnetoresistive RAM" IEEE International Solid-State Circuits Conference (2001) TA 7.6.				
EXAMINER SON MAI			DATE CONSIDERED 05/11/04			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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